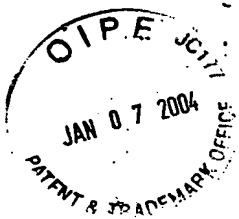
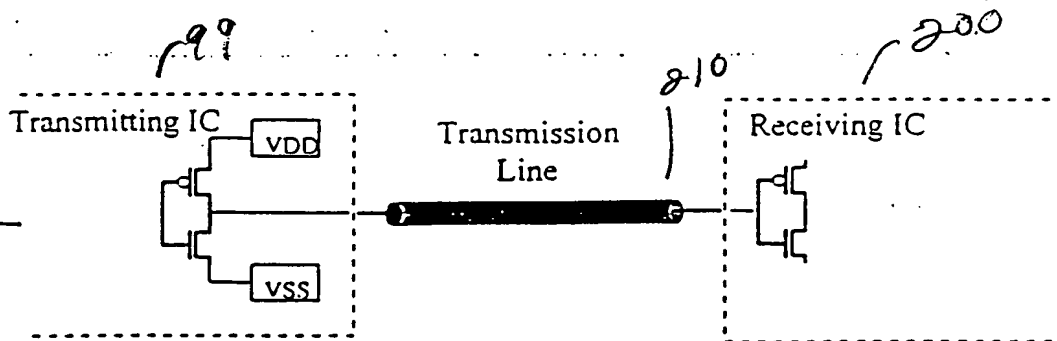


-PRIOR ART-

Figure 1 column parallel architecture for focal plane A/D conversion



- PRIOR ART -



(a) Fig 2

- PRIOR ART -

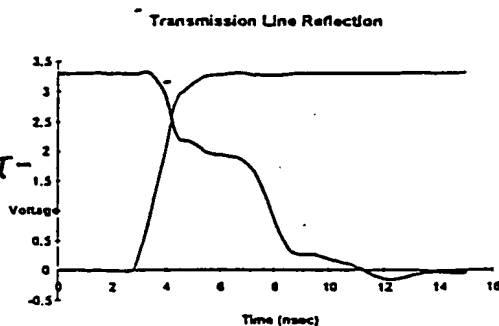
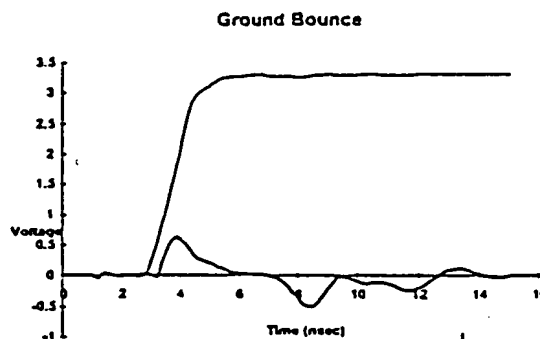


Fig (b) 3a



(c) Fig 3b

Figure 1 (a) Conceptual diagram of standard CMOS I/O. (b) Output waveform when driving 1ft of coax cable. (c) Glitch voltage at the transmitter ground line.

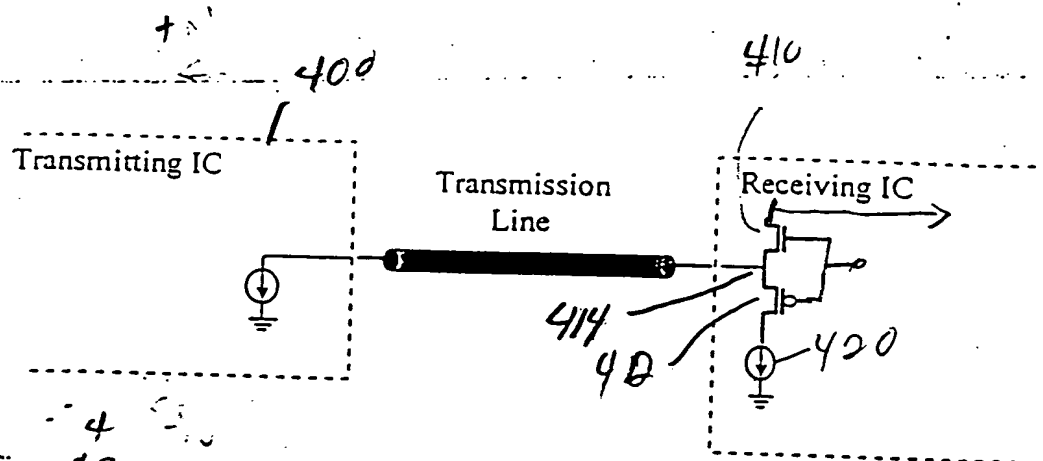
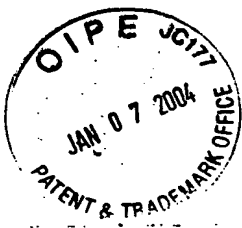


Figure 7 Conceptual sketch of CMOS current mode I/O technique